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TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>		Application Number	10/734,082
		Filing Date	December 10, 2003
		First Named Inventor	Ramesh Yarlagadda
		Group Art Unit Number	2189
		Examiner Name	Woo H. Choi
Total Number of Pages in This Submission	14	Attorney Docket Number	23019-07460

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REMARKS:

SIGNATURE OF ATTORNEY OR AGENT		
Signature:	<i>Greg T. Sueoka</i>	
Attorney/Reg. No.:	Greg T. Sueoka, Reg. No.: 33,800	Dated: 5/17/06

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IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Ramesh Yarlagadda, Shwetal Desai, and Harish Devanagondi
SERIAL NO.: 10/734,082
FILING DATE: December 10, 2003
TITLE: High Bandwidth Memory Management Using Multi-Bank DRAM Devices
EXAMINER: Woo H. Choi
GROUP ART UNIT: 2189
ATTY. DKT. NO.: 23019-07460

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Greg T. Sueoka, Reg. No.: 33,800

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AMENDMENT A AND RESPONSE UNDER 37 CFR § 1.111

Sir:

This amendment for the patent application identified above is in response to the Office action dated February 17, 2006, which set a shortened statutory period for response that expires on May 17, 2006.

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

Kindly amend this application as indicated herein.